

## CLAIMS

1. (Deleted)
- 5 2. (Deleted)
3. (Amended) A Doherty amplifier comprising:  
an input terminal;  
input branching means for distributing a signal applied from said  
10 input terminal to a first path and a second path;  
a carrier amplifier for amplifying a signal distributed to the first  
path by said input branching means;  
a peak amplifier for amplifying a signal of a predetermined level or  
higher among signals distributed to the second path by said input branching  
15 means;  
output combining means for combining an output of said carrier  
amplifier with an output of said peak amplifier; and  
a gain compensator disposed at a position ahead of said peak  
amplifier in the second path for changing a gain in accordance with the level  
20 of an input signal in order to correct the level of the signal distributed to the  
second path,  
said carrier amplifier and said peak amplifier being devices having  
the same characteristics,  
said peak amplifier having a gain smaller than an ideal gain,  
25 wherein said gain compensator has a larger gain, when a signal  
equal to or higher than the predetermined level is applied, than a gain when

a signal lower than the predetermined level is applied, said gain being set based on a transfer conductance of said peak amplifier.

4. (Deleted)

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5. (Deleted)

6. (Amended) The Doherty amplifier according to claim 3, wherein said gain compensator comprises a parallel circuit composed of an anti-parallel diode and a resistor, or a parallel circuit composed of a diode and a resistor, or an FET, or a bipolar transistor.

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7. (Amended) The Doherty amplifier according to claim 3, wherein said carrier amplifier and said peak amplifier are each composed of an FET, and said gain compensator compensates said peak amplifier for a gm characteristic.

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